

ABSTRACT OF THE DISCLOSURE

An active matrix liquid crystal display element capable of reducing flicker comprises: a plurality of source lines; a plurality of gate lines arranged so as to intersect the plurality of source lines in a plan view, for transmitting a gate signal; a plurality of pixels defined by the plurality of source lines and the plurality of gate lines, constituting an image display plane; a pixel electrode provided for every pixel; an opposed electrode facing the pixel electrode across a liquid crystal layer; a storage capacitor for holding a voltage applied between its corresponding pixel electrode and the opposed electrode; and a pixel transistor having a source electrode, a drain electrode and a gate electrode which are connected to a corresponding one of the source lines, a corresponding one of the pixel electrodes and a corresponding one of the gate lines respectively, and being turned ON or OFF by the gate signal, wherein an index B given by $B = L_{st}/L_{gd}$ is equal to or greater than 7, where a periphery length of the storage capacitor is L_{st} and a periphery length of a gate electrode to pixel electrode capacitor of the pixel transistor is L_{gd} .